Consider the following piece of x86-64 assembly code, where each instruction is assigned an ID:

```
1. mov -8(%rbp), %r10
2. add %r15, %r10
3. mov %r10, -16(%rbp)
4. mov -24(%rbp), %r10
5. add %r10, %r11
6. mul %r10, %r12
7. add %r11, %r12
```

Draw dependence edges of the instructions (a source node of a dependence edge is an instruction A that must execute before the destination instruction B):

①   ②   ③   ④   ⑤   ⑥   ⑦
1   2   3   4   5   6   7